

Broadcom Product Catalog

Wireless chipsets

PCI/PCIe/USB

Chip	Radio chip	WLC	PHY	Interface	PHY modes	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM4301	BCM2051	2	B	PCI/CB	b	2002-08-23	WLAN baseband processor and MAC functionality	<u>11 devices</u>	<u>8 devices</u>	<u>PR RD</u>
BCM4307	BCM2051	2	B	PCI/CB	b	2002-06-14	V.92 modem and 10/100 Ethernet capability in chip	<u>4 devices</u>	<u>0 devices</u>	<u>PR RD</u>

(a)bg

Chip	Radio chip	W L C	P H Y	Inter face	PH Y mo des	Fir st se en (F CC)	Not es	Adap ters	ESys tems	PR / PP age
BCM4306 r2	BCM2050	4	G	PCI / CB / PCMCIA /...	bg	2002-12-12 (all 4306s)		89 devices (all 4306s)	59 devices (all 4306s)	PR RD
BCM4306 r3	BCM2050	5	G	PCI / CB / PCMCIA /...	bg	2002-12-12 (all 4306s)		89 devices (all 4306s)	59 devices (all 4306s)	PR RD
BCM4309 r2	BCM2050 (b/g) BCM2060 (a)	4	A + G	PCI / CB	abg	2003-03-14 (all 4309s)	BCM2050 may be omitted for 802.11a only use	6 devices (all 4309s)	2 devices (all 4309s)	PR
BCM4309 r3	BCM2050 (b/g) BCM2060 (a)	5	A + G	PCI / CB	abg	2003-03-14 (all 4309s)	BCM2050 may be omitted for 802.11a only use	6 devices (all 4309s)	2 devices (all 4309s)	PR
BCM4311	BCM2050	10	G r8	PCIe	bg	2005-07-22	.	7 devices	0 devices	PPage

Chip	Radio chip	W L C	P H Y	Inter face	PH Y mo des	Fir st se en (F CC)	Not es	Adap ters	ESys tems	PR / PP age
BCM43 11	BCM2050 (b/g) BCM2060 (a)	10	...	PCle	abg	2005-08-03	.	<u>5 devices</u>	<u>0 devices</u>	<u>PPage RD</u>
BCM43 12	BCM2050	15	LP r 1	PCle	bg	2007-07-28	.	<u>15 devices</u>	<u>5 devices</u>	<u>PPage PBrief</u>
BCM43 18	BCM2050	9	G r7	PCI / CB	bg	2004-12-09	Single-chip 802.11 b/g, MAC and Radio	<u>31 devices</u>	<u>65 devices</u>	<u>PR RD</u>
BCM43 18E	BCM2050	9	G r7	PCI / CB / ...	bg	2004-12-09	<u>on-board</u> <u>Encore</u> <u>DSP</u> (BroadRa <u>nge®</u>)	<u>29 devices</u>	<u>13 devices</u>	<u>PPage</u>
BCM43 18E	BCM2050 (b/g) BCM2060 (a)	9	...	PCI / CB / ...	abg	2005-03-08	<u>on-board</u> <u>Encore</u> <u>DSP</u> (BroadRa <u>nge®</u>)	<u>7 devices</u>	<u>0 devices</u>	<u>PPage RD</u>

Chip	Radio chip	WLC	PHY	Interface	PHY modes	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM4333 + BCM4306	BCM2050	4/5	G	USB 2.0	bg	2002-12-16	BCM4333 (likely a rebranded BCM4702) serves as a bridge chip for BCM4306, used over USB w/ RNDIS mode	<u>2 devices</u>	<u>0 devices</u>	<u>PR</u>
BCM4712	BCM2050	7	Gr2	USB 2.0	bg	2003-11-24	router WiSoC, RNDIS mode	<u>3 devices</u>	<u>46 devices</u>	<u>PPage</u>
BCM4320	BCM2050	...	...	USB 2.0	bg	2005-01-05	Single-Chip 802.11g USB 2.0 Transceiver	<u>16 devices</u>	<u>0 devices</u>	<u>PPage</u>
BCM4326U	...	...	...	USB 2.0	bg	2006-11-13	Low-Power 802.11bg	<u>9 devices</u>	<u>0 devices</u>	<u>PPage</u> <u>PBrief</u>

Chip	Radio chip	WLC	PHY	Interface	PHY modes	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
							Transceiver			

(a)bgn

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM4313	BCM2064	24	LCN _{r1}	PCIe	bgn	1x1:1	2009-10-01	HT20-only (72MBps max)	<u>11</u> devices	<u>9</u> devices	<u>PR</u> PPage
BCM43131	BCM2057 _{r14}	30	<u>N_{r17}</u>	PCIe	bgn	1x1:1	2013-09-17		<u>1</u> devices	<u>2</u> devices	<u>PR</u>
BCM43142	BCM2065	33	LCN _{40 r3}	PCIe + USB (BT)	bgn	1x1:1	2011-11-09	Wi-Fi Direct support, BT4.0+HS	<u>6</u> devices	<u>0</u> devices	<u>PPage</u>
BCM43	<u>BCM20</u>	11/12	N _{r1}	PCI / CB	bgn	2x2:2	2006-	draft-n; abgn/bgn	<u>13</u>	<u>19</u>	<u>PR</u>

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
21	55r4	L11/1		/ PCIe			04-12	depending on implementation	devices	devices	
BCM4321	BCM2055r4	11/12	Nr1	PCI / CB / PCIe	abgn	2x2:2	2006-06-14	draft-n; abgn/bgn depending on implementation	11 devices	3 devices	PR
BCM43217	BCM2057r14	30	Nr17	PCIe	bgn	2x2:2	2012-06-28		5 devices	22 devices	PR
BCM4322	BCM2056r3	16	Nr4	PCI / PCIe	abgn	2x2:2	2008-02-12	802.11 abgn/bgn depending on implementation	13 devices	16 devices	PPage
BCM43222	BCM2056r6	16	Nr6	PCI	abgn	2x2:2	2009-06-09	802.11 abgn/bgn depending on implementation	4 devices	15 devices	PR

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM43224	BCM2056r11	23	N r6	PCle	abgn	2x2:2	2009-04-02		9 devices	13 devices	RD PPage
BCM43225	BCM2056r11	23	N r6	PCle	bgn	2x2:2	2009-03-25		11 devices	15 devices	PR RD
BCM43227	BCM2057 r9	30	N r16	PCle	bgn	2x2:2	2010-11-10		4 devices	5 devices	PR
BCM43228	BCM2057 r9	30	N r16	PCle	abgn	2x2:2	2010-12-17		11 devices	7 devices	PR
BCM4323	...	...	...	USB 2.0	abgn	2x2:2	2009-04-10	BCM4322 w/ USB IF used	24 devices	0 devices	PPage
BCM43231	...	...	...	USB 2.0	bgn	2x2:2	2008-09-02	BCM4322 w/ USB IF used, single-band only	12 devices	6 devices	PR
BCM43234	...	...	...	USB 2.0	bgn	1x1:1	2011-03-29		3 devices	2 devices	PR

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM43235	...	...	...	USB 2.0	bgn	2x2:2	2011-04-28		<u>4</u> devices	<u>1</u> devices	PPage family
BCM43236	BCM2057 r7	24	...	USB 2.0	abgn	2x2:2	2010-11-17	publicly released 43236es appear to be rev.2 chips	<u>15</u> devices	<u>19</u> devices	PPage family
BCM43236B	...	...	...	USB 2.0	abgn	2x2:2	2011-10-26	seem to be rev.3 43236es	<u>6</u> devices	<u>10</u> devices	PR
BCM43237	...	...	...	USB 2.0	bgn	2x2:2	2011-09-28		<u>1</u> devices	<u>0</u> devices	PR
BCM43238	...	...	...	USB 2.0	bgn	2x2:2	2012-11-29		<u>0</u> devices	<u>1</u> devices	PPage family
BCM4331	BCM2059 r0	29	HT r1	PCIe	abgn	3x3:3	2010-10-20		<u>7</u> devices	<u>41</u> devices	PPage
BCM43	...	...	...	PCIe	abgn	3x3:3	2012-		<u>0</u>	<u>8</u>	PR

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
431			.				07-06		devices	devices	

ac

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config (max .rate)	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM43162	...	...	...	PCIe	abgn+ac	1x1:1 (433Mbps)	2014-02-13	DB 2.4/5GHz, 802.11ac 256-QAM, LDPC, PCIe 2.0 ID 14e4:43? ?	1 devices	0 devices	PR
BCM43516	...	...	...	USB 2.0	abgn+ac	1x1:1 (433Mbps)		DB 2.4/5GHz, 802.11ac 256-QAM, LDPC, USB 2.0	0 devices	0 devices	PR PPage

Chi p	Ra dio chi p	W L C	P H Y	Inte rfac e	PH Y mo de s	MIM O conf ig (max .rate)	Fir st se en (F C C)	Not es	Ada pter s	ESy stem s	PR / PP ag e
BCM4 352	BCM2 069 r3/4?	40	AC	PCle	abgn+ ac	2x2:2 (867Mb ps)	2012- 05-16	DB 2.4/5GHz, 802.11ac 256-QAM, LDPC, PCle 2.0 ID 14e4:43b 1	<u>7</u> devices	<u>16</u> devices	<u>PR</u> <u>PPag</u> <u>e</u>
BCM4 3520	...	...		PCle	abgn+ ac	2x2:2 (867Mb ps)		DB 2.4/5GHz, 802.11ac 256-QAM, LDPC, PCle 2.0 ID 14e4:43? ?	<u>0</u> devices	<u>0</u> devices	<u>PR</u>
BCM4 3526	...	...	...	USB 2.0	abgn+ ac	2x2:2 (867Mb ps)	2012- 08-20	DB 2.4/5GHz, 802.11ac 256-QAM, LDPC, USB 2.0	<u>8</u> devices	<u>3</u> devices	<u>PR</u> <u>PPag</u> <u>e</u>
BCM4	...	...		PCle	abgn+	2x2:2 (867Mb	2014-	int. BT 4.1, ID	<u>1</u>	<u>0</u>	<u>PR</u>

Chi p	Ra dio chi p	W L C	P H Y	Inte rfac e	PH Y mo de s	MIM O conf ig (max .rate)	Fir st se en (F C C)	Not es	Ada pter s	ESy stem s	PR / PP ag e
356					ac	ps)	11-20	14e4:43ec supported in 3.17+	devices	devices	drv
BCM4 3566	...	...	...	USB 3.0	abgn+ ac	2x2:2 (867Mb ps)		int. BT 4.1, ID 0a5c:bd27 supported in 3.17+	0 devices	0 devices	PR drv
BCM4 3567	...	...		PCle	abgn+ ac	2x2:2 (867Mb ps)		int. BT 4.1, ID 4e4:43d3 supported in 3.17+	0 devices	0 devices	PR drv
BCM4 3569	...	...	...	USB 3.0	abgn+ ac	2x2:2 (867Mb ps)		int. BT 4.1, ID 0a5c:bd27 supported in 3.17+	0 devices	0 devices	PR drv
BCM4 3570	...	...		PCle	abgn+ ac	2x2:2 (867Mb ps)		int. BT 4.1, ID 14e4:43d9	0 devices	0 devices	PR drv

Chip	Radio chip	WLC	PHY	Interface	PHY modes	MIMO config (max rate)	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
								supported in 3.17+			
BCM4358	...	...	...	PCIe	abgn+ac	2x2:2 (867Mbps)	Q3 2014	int. BT, PA, LNA, TurboQAM	<u>0</u> devices	<u>0</u> devices	<u>PR</u>
BCM43460	...	...		PCIe	abgn+ac	3x3:3 (1.3Gbps)	2013-04-18	5G WiFi 3x3 2.4/5GHz 802.11ac MAC/PHY /Radio	<u>1</u> devices	<u>8</u> devices	<u>PPage</u>
BCM43462	...	...		PCIe	abgn+ac	3x3:3 (1.3Gbps)		dual-band 2.4/5GHz, int. MAC, PHY, radio	<u>0</u> devices	<u>0</u> devices	<u>PR</u>
BCM4360	BCM2069 r3/4?	40	AC	PCIe	abgn+ac	3x3:3 (1.3Gbps)	2012-04-20	DB 2.4/5GHz, 802.11ac 256-QAM, LDPC, PCIe 2.0 ID 14e4:436	<u>10</u> devices	<u>55</u> devices	<u>PR PPage</u>

Chi p	Ra dio chi p	W L C	P H Y	Inte rfac e	PH Y mo de s	MIM O conf ig (max .rate)	Fir st se en (F C C)	Not es	Ada pter s	ESy stem s	PR / PP ag e
								0			
BCM4 3602	...	...		PCle	abgn+ ac	3x3:3 (1.3Gbp s)	2014- 06-02	int. 320MHz ARM Cortex R4, 960KB SRAM, ID 14e4:43b a (FullMAC arch. targeted), pin compatibl e w/ BCM4360	<u>1</u> devices	<u>5</u> devices	<u>PR</u> <u>SNB</u> <u>drv</u>
BCM4 366	...	...		PCle	abgn+ ac	4x4:4 (1.7Gbp s)		4x4 MU- MIMO, 160 MHz NitroQAM (QAM- 1024) <u>D-Link</u> <u>DIR-</u> <u>895L/R</u>	<u>0</u> devices	<u>1</u> devices	<u>PR P</u> <u>R</u> <u>PPag</u> <u>e</u>

SDIO/Other

(a)bg(n)

Chip	Radio	WLC	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM43143	...	...	SDIO / USB	bgn	1x1:1	2013-09-17	802.11bgn MAC/PHY/ Radio with USB/SDIO Host Interface	<u>3</u> devices	<u>1</u> devices	<u>PR</u> PBrief
BCM4319	...	...	USB 2.0 /...	abgn / bgn	1x1:1	2010-03-31	BT 2.1 + EDR, FM radio	<u>6</u> devices	<u>0</u> devices	<u>PR</u> PR
BCM43241	...	...	SDIO	abgn	2x2:2	2013-04-05	2x2 MIMO dualband combo device, 802.11n, BT 4.0+HS, FM receiver	<u>1</u> devices	<u>2</u> devices	<u>PPage</u> PBrief
BCM43242	...	...	SDIO / USB	abgn	2x2:2	2013-09-27	802.11abgn, 2x2 MIMO, VSDB, BT 4.0+HS,	<u>3</u> devicesj	<u>0</u> devices	<u>PR</u>

Chip	Radio	WLC	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
							LDPC, STBC			
BCM4325	...	...	SDIO	abg / bg	n/a	2010-10-07	BT 2.1 + EDK, FM radio	<u>0 devices</u>	<u>1 devices</u>	<u>PR PPage</u>
BCM4328	...	...	SDIO	abg	n/a		Low-Power 802.11 a/b/g Transceiver	<u>0 devices</u>	<u>0 devices</u>	<u>PPage PBrief</u>
BCM4329	...	...	SDIO	abgn / bgn	1x1:1	2011-02-17	BT 2.1+EDR FM radio	<u>2 devices</u>	<u>0 devices</u>	<u>PPage</u>
BCM4330	...	...	SDIO	abgn / bgn	1x1:1	2011-10-04	BT 4.0+HS, FM radio <u>Apple iPhone 4S</u>	<u>3 devices</u>	<u>8 devices</u>	<u>PPage</u>
BCM4334	...	...	SDIO	abgn	1x1:1	2013-01-29	BT 4.0+HS, FM <u>Apple iPhone 5 /</u>	<u>1 devices</u>	<u>1 devices</u>	<u>PPage</u>

Chip	Radio	WLC	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
							5c			
BCM43340	...	...	SDIO	abgn	1x1:1	2013-06-19	BT 4.0 + EDR, FM, SDIO	<u>1 devices</u>	<u>1 devices</u>	<u>PBrief</u>
BCM43341	...	...	SDIO	abgn	1x1:1		BT 4.0 + EDR, NFC + FM, SDIO	<u>0 devices</u>	<u>0 devices</u>	<u>PPage</u>
BCM43342	...	...	SDIO	abgn	1x1:1		BT 4.0 + EDR, FM <u>Apple iPhone 5s</u>	<u>0 devices</u>	<u>0 devices</u>	<u>iPhone wiki</u>
BCM4336	...	...	SDIO, prob.	bgn	1x1:1	2011-06-29	used in <u>Roku LT</u> , <u>Roku 2</u> players	<u>1 devices</u>	<u>4 devices</u>	<u>PR</u>
BCM43362	...	...	SDIO	bgn	1x1:1	2011-03-07	used in <u>Roku 2500X</u> player	<u>7 devices</u>	<u>5 devices</u>	<u>PR</u>
BCM43	...	...	SDIO	bgn	1x1:1		2.4GHz	<u>0</u>	<u>0</u>	<u>PR</u>

Chip	Radio	WLC	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
430		.					1T1R, BT 4.1/Smart, Rezenice int. PA/LNA, TurboQAM™, LDPC, BF	devices	devices	
BCM43438	...	...	SDIO	bgn	1x1:1		BT 4.1, used in Ingenic Newton 2 T7GD2 MB WFA56441 10/13/2014	0 devices	0 devices	CNX

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Chip	Radio	WLC	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PPage
BCM4335	...	...	SDIO 3.0 / PCIe /	abgn+ac	1x1:1	2013-02-20	BT 4.0+HS, FM radio,	0 devices	11 devices	PR PPage

Chip	Radio	WLC	Interface	PHY modes	MIMO config	First seen (FCC)	Notes	Adapters	ESystems	PR / PP age
			HSIC				Samsung Galaxy S4			
BCM4339	...	...	SDIO	abgn+ac	1x1:1	2013-09-04	updated version of the BCM4335 Samsung Galaxy Note 3	<u>0</u> devices	<u>3</u> devices	<u>PR</u>
BCM4345	...	...	SDIO	abgn+ac	2x2:2		802.11ac, BT 4.1 combo chip Apple iPhone 6 / 6+	<u>0</u> devices	<u>0</u> devices	<u>TD</u>
BCM43455	...	...	SDIO / PCIe	abgn+ac	1x1:1		1x1 HT80 802.11ac, 2.4/5GHz BT 4.1/BLE, FM radio, SDIO 3.0, PCIe LDPC, Beamfor	<u>0</u> devices	<u>0</u> devices	<u>PR</u> <u>PR</u>

Chip	Radio	WLC	Interface	PHY modes	MIMO config	Fir st se en (FCC)	Not es	Adap ters	ESyst ems	PR / PP age
							ming, LTE coex			
BCM4354	...	...	SDIO 3.0 / PCIe / USB	abgn+ac	2x2:2		BT 4.1/BLE, FM radio, SDIO 3.0 PCIe-WLAN, UART/USB-BT, Samsung Galaxy S5	0 devices	0 devices	PR Blog WFA
BCM4359	...	...	SDIO 3.0 / PCIe / USB	abgn+ac	2x2:2		2x2 HT80 802.11ac, 2.4/5GHz BT 4.2/BLE, FM radio, SDIO 3.0, RSDB, PCIe-WLAN, UART/USB-BT	0 devices	0 devices	PR PR

Internet of Things (IoT)

- "WICED" (Wireless Internet Connectivity for Embedded Devices)

is Broadcom trademark/brand for Internet-of-Things chips

(a)bgn

Chip	Arch.	Clock speed	Cores	PHY modes	MI MO config	First seen (FCC)	Notes	Adapters	ESystems	PPage / PR / PDF
BCM43907	ARM Cortex-R4	320 MHz	1	abgn	1x1:1	(2014)	WICED, 802.11abgn 1x1 WiSoC with audio	<u>0 devices</u>	0 devices	<u>PR</u>
BCM43909	ARM Cortex-R4	320 MHz	1	abgn	1x1:1	(2014)	WICED, 802.11abgn WiSoC (USB 0a5c:bd28)	<u>0 devices</u>	0 devices	
BCM44362	ARM Cortex-M3	320 MHz	1	bgn	1x1:1	(2013)	WICED 802.11bg n WiSoC Serial, UART, SPI, <u>USB</u>	<u>1 devices</u>	0 devices	<u>PR</u>

- BCM43907 - (2014) 802.11abgn 1x1 WiSoC with audio

SoC

WiSoC

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Chip	Radio	WLC rev.	PHY rev.	Arch.	Clock speed	PHY modes	First seen (FCC)	Notes	ESystems	PR / PPage
<u>BCM4712</u>	BCM2050	7/2	G r2	<u>BMIPS3300</u>	200 MHz	bg	2003-11-24		<u>46 devices</u>	<u>PPage RD RG</u>
<u>BCM5350</u>	BCM2050	7/2	G r2	<u>BMIPS3300</u>	200 MHz	bg	2004-07-21	in. FE switch	<u>13 devices</u>	<u>PR RD</u>
<u>BCM5352</u>	BCM2050	9/7	G r7	<u>BMIPS3300</u>	200 MHz	bg	2005-03-29	int. FE switch	<u>5 devices</u>	.
<u>BCM5352E</u>	BCM2050	9/7	<u>G r7</u>	<u>BMIPS3300</u>	200 MHz	bg	2005-05-17	int. FE switch, <u>Encore DSP</u>	<u>16 devices</u>	<u>PPage</u>
<u>BCM5354</u>	BCM2062 r1	<u>13/0</u>	<u>LP r0</u>	<u>BMIPS3300</u>	240 MHz	bg	2007-01-09	int. FE switch, <u>Encore DSP</u>	<u>41 devices</u>	<u>PR RD PPage</u>
BCM5355	...	...	...	<u>MIPS74K</u>	333 MHz	bg	2009-09-17	int. FE switch, seems to identify as BCM5355	<u>1 devices</u>	

Chip	Radio	WLC rev.	PHY rev.	Arch.	Clock speed	PHY modes	First seen (FCC)	Notes	ESystems	PR / PPage
								6		

(a)bgn

Chip	Radio	WLC/PHY rev.	Arch.	Clock speed	PHY modes	MI MO config	First seen (FCC)	Notes	ESystems	PR / PPage
BCM5356	...	A1 21/3 B0 28/9 C0 28/17	MIPS 74K	300 MHz	bgn	1x1:1	2009-07-03	int. FE switch	20 devices	PR PPage family
BCM5356U	...	B0 28/9	MIPS 74K	300/(500)* MHz	bgn	1x1:1	2011-07-19	int. FE switch, USB	4 devices	PPage family
BCM5357	BCM2057	B0 28/9 C0 28/17	MIPS 74K	300 MHz	bgn	2x2:2	2010-05-18	int. FE switch, USB 2.0 IF	39 devices	PPage
BCM53	BCM20	B0 28/9	MIPS	500	bgn	2x2:2	2011-	int. FE switch,	7	PPage

Chip	Radio	WLC/PHY rev.	Arch.	Clock speed	PHY modes	MI MO config	First seen (FCC)	Notes	ESystems	PR / PPage
58	57	C0 28/17?	74K	MHz			12-01	USB 2.0, VoIP	devices	family
BCM5358U	...	B0 28/9	MIPS 74K	500 MHz	bgn	2x2:2	2011-01-28	int. FE switch, USB 2.0, PCIe	25 devices	PPage family
BCM4716	BCM2056 r7	B0 17/6	MIPS 74K	300 MHz	bgn	2x2:2	2008-12-30	2.4GHz, 802.11n MAC block diagram cpu	8 devices	PPage
BCM4717	...	A1 17/5	MIPS 74K	354 MHz	abgn	2x2:2	2008-12-30	1x USB 2.0 block diagram	9 devices	PPage
BCM4717V	BCM2056 r7	A1 17/5	MIPS 74K	354 MHz	abgn	2x2:2	2011-02-18	802.11n 2.4/5GHz wireless STB SoC	1 devices	PPage

Chip	Radi o	WLC/ PHY rev.	Ar ch.	Clock spee d	PH Y mo des	MI MO co nfi g	Fir st se en (F CC)	No tes	ESyst ems	PR / PP age
<u>BCM4718</u>	<u>BCM2056</u>	<u>A1 17/5</u>	<u>MIPS 74Kc</u>	<u>480/533</u> MHz	abgn	2x2:2	2009-01-23	PCIe IF, 2x USB2.0, <u>A1 block diagram</u> <u>cpu</u>	<u>27</u> <u>devices</u>	<u>PPage</u>
BCM47186	<u>BCM2057</u>	<u>B0 28/9</u>	<u>MIPS 74K</u>	480 MHz[1]	abgn	2x2:2	2011-03-31	5-port FE switch, PCIe, int.2.4G Hz PA	<u>7</u> <u>devices</u>	<u>PPage</u>
BCM4748	...	A1 ?/?	<u>MIPS 74K</u>	533 MHz	abgn	2x2:2	2010-10-01	2x2 802.11n 2.4/5GHz SoC	<u>5</u> <u>devices</u>	<u>PPage</u> <u>PBrief</u>

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Chip	Arc h.	Cloc k spe ed	Cor es	PHY mod es	MIM O conf ig	Firs t see n (FC C)	Not es	ESyste ms	PR / PPa ge
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Chip	Arch.	Clock speed	Cores	PHY modes	MIMO config	First seen (FCC)	Notes	ESystems	PR / PPage
BCM47189	ARM	800 MHz	1	abgn+ac	2x2:2		DB 2.4/5GHz, int. 2x2/1+1 5G w/ CPU, FE switch ARM CPU, PCIe, USB 2.0, RGMII/GigE, iPA/ePA	0 devices	PPage PR PR
BCM53573	ARM	800 MHz	1	abgn+ac	2x2:2		DB 2.4/5GHz, int. 2x2/1+1 5G w/ CPU, FE switch ARM CPU, PCIe, USB 2.0, RGMII/GigE, iPA/ePA	0 devices	PPage PR PR

General Purpose

MIPS

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PR / PPage	PBrief
BCM5351	BMIPS3300		1		PCI IF	1 devices		
BCM5364P	BMIPS3300	200 MHz	1	2004-02-24	PCI IF, 5-port FE switch w/ int. PHY (TX/FX), UART SoC 200MHz MIPS32 BCM5364/BCM5364P (PCI)	1 devices	PPage	PR
BCM5836P	BMIPS3300	264 MHz	1	2007-03-29	with PCI & VPN/IPSec (AES/3DES)	1 devices	PPage	PBrief PG DS
BCM4780P	BMIPS3300	300 MHz	1		PCI IF	1 devices	PB	PBrief
BCM4702	BMIPS3300	125 MHz	1	2002-04-30	2x PCI IF	41 devices	PR	PBrief
BCM4703	BMIPS3300	266 MHz	1	2007-11-30	2x PCI IF?, 2x FE MAC, ident.as BCM4704	3 devices	PPage	

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PR / PPage	PBrief
BCM4704	BMIPS3300	266 / 300 MHz	1	2005-01-12	2x PCI IF, 2x FE MAC	20 devices	PPage	
BCM4705	BMIPS3300	300 MHz	1	2006-09-29	2x PCI, int. GbE MAC, USB 2.0	6 devices	PPage	.
BCM4706	MIPS74Kc	600 MHz	1	2011-07-29	2x PCIe, int.GbE MAC, USB 2.0, Ser/Par/NAND flash support	21 devices	PPage	
BCM53001	MIPS74Kc	400 MHz	1		2x PCIe IF, 2x GbE MAC, RGMII, DDR2, USB 2.0	0 devices	PPage	PBrief
BCM53003	MIPS74Kc	600 MHz	1	2014-05-22	2x PCIe IF, 2x GbE MAC, RGMII, DDR2, USB 2.0	1 devices	PPage	PBrief

ARM

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
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Chip	Arch. .	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage e	PR/P B
BCM4707	ARM Cortex A9	800 MHz	2		2x PCIe IF, int. GbE SW + 5x GbE PHYs, USB 3.0 IF, Ser./Par./NAN D flash support	0 devices	PPage series	PR
BCM4708	ARM Cortex A9	800 MHz	2	2013- 02-07	2x PCIe IF, int. GbE SW + 5x GbE PHYs, USB 3.0 IF (prob.), Ser./Par./NAN D flash support	19 devices	PPage series	PR
BCM4708 1	ARM Cortex A9	800 MHz	1	2012- 12-28	2x PCIe IF, int. GbE SW + 5x GbE PHYs, USB 3.0 IF, Ser./Par./NAN D flash support, BCM47081 presumed to be quite similar to BCM4708 (perhaps	14 devices	PPage series	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
					single-core only)			
BCM4709	ARM Cortex A9	1 GHz	2	2013-08-07	2x PCIe IF, int. GbE SW + 5x GbE PHYs, USB 3.0 IF, Ser./Par./NAND flash support	13 devices	PPage series	PR
BCM47094	ARM Cortex A9	1.4 GHz	2		2x PCIe IF, int. GbE SW + 5x GbE PHYs, USB 3.0 IF, Ser./Par./NAND flash support	1 devices	PPage	PR PR
BCM53010	ARM Cortex A9	800 MHz	1		single core Cortex-A9 CPU at 800 MHz, 256KB L2 cache, 16-bit DDR2 interface, integrated switch, GPHYs and packet	0 devices	PPage series	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
					accelerator			
BCM5301 1	ARM Cortex A9	800 MHz	2		dual core Cortex-A9 CPU at 800 MHz, 256KB L2 cache, 16-bit DDR2 interface, USB 3.0, integrated switch, GPHYs and packet accelerator	0 devices	PPage series	PR
BCM5301 2	ARM Cortex A9	1 GHz	2		dual core Cortex-A9 CPU at 1 GHz, 256KB L2 cache, 16-bit DDR2 interface, USB 3.0, integrated switch, GPHYs and packet accelerator	0 devices	PPage series	PR
BCM5301 4	ARM Cortex A9	800 MHz	1		single core Cortex-A9 CPU at 800 MHz, 256KB L2	0 devices	PPage series	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
					cache, 16-bit DDR2 interface, integrated switch, GPHYs and packet accelerator			
BCM53015	ARM Cortex A9	800 MHz	2		dual core Cortex-A9 CPU at 800 MHz, 256KB L2 cache, 16-bit DDR2 interface, USB 3.0, integrated switch, GPHYs and packet accelerator	0 devices	PPage series	PR
BCM53016	ARM Cortex A9	1 GHz	2	2014-03-19	dual core Cortex-A9 CPU at 1 GHz, 256KB L2 cache, 16-bit DDR2 interface, USB 3.0, integrated switch, GPHYs and packet	3 devices	PPage series	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
					accelerator			
BCM53017	ARM Cortex A9	1.1 GHz	2		dual Cortex-A9 CPU up to 1.1 GHz, 512KB L2 cache (including ECC), 16-bit DDR2 interface, USB 3.0, integrated switch, GPHYs and packet accelerator	0 devices	PPage series	PR
BCM53018	ARM Cortex A9	1.1 GHz	2		dual Cortex-A9 CPU up to 1.1 GHz, 512KB L2 cache (including ECC), 16-bit DDR2 interface, USB 3.0, integrated switch, GPHYs and packet accelerator	0 devices	PPage series	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
BCM53019	ARM Cortex A9	1 GHz	2	2013-06-10	likely quite similar to the BCM5301x series SoCs	3 devices	PPage series	PR
BCM58622	ARM Cortex A9	1.2 GHz	1		GbE, 1x SATA 3, PCIe, USB 3.0, RAM DDR3, likely quite similar to the BCM5852x series SoCs	0 devices	PPage series	PR
BCM58623	ARM Cortex A9	1.2 GHz	1		GbE, 2x SATA 3, PCIe, USB 3.0, RAM DDR3, likely quite similar to the BCM5852x series SoCs	0 devices	PPage series	PR
BCM58525	ARM Cortex A9	1.2 GHz	2		GbE, 2x SATA 3, PCIe, USB 3.0, RAM DDR3, likely quite similar to the BCM5852x series SoCs	0 devices	PPage series	PR PR

Multimedia Processors

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
BCM2763	ARM 1176JZF-S	600 MHz	1		Full HD 1080p HP H.264 Video Encode/Decode DualCore VideoCoreIV Multimedia Processor	0 devices	PPage	
BCM2835	ARM 1176JZF-S	700 MHz	1	2011-06-29	Full HD 1080p HP H.264 Video Encode/Decode DualCore VideoCoreIV Multimedia Co-Processor	10 devices	PPage	PR PR Roku
BCM2836	ARMv7 Cortex A7	900 MHz	4		Full HD 1080p HP H.264 Video Encode/Decode DualCore VideoCoreIV Multimedia Co-Processor	1 devices	PPage^	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
BCM7208	ARM 1176JZF-S	400 MHz	1	2012-02-22	Full HD 1080p HP H.264 Video Encode/Decode DualCore VideoCoreIV Multimedia Co-Processor	1 devices	PPage^	Roku
BCM7218	ARM 1176JZF-S	600 MHz	1	2013-09-17	Full HD 1080p HP H.264 Video Encode/Decode DualCore VideoCoreIV Multimedia Co-Processor	2 devices	PPage^	Roku
BCM11130	ARM Cortex A9	900 MHz	2	2013-02-08	Full HD 1080p HP H.264 Video Encode/Decode DualCore VideoCoreIV Multimedia Processor	1 devices	PPage^	PR Roku
BCM1114	ARM	1 GHz	2		Full HD 1080p HP H.264	0 devices	PPage^	PR

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Notes	ESystems	PPage	PR/PB
0	Cortex A9				Video Encode/Decode DualCore VideoCoreIV Multimedia Processor			

Cable Modem

Chip	Arch.	Clock speed	Cores	First seen (FCC)	DOCSIS	Notes	ESystems	PR / PPage	pdf
BCM3345	MIPS32	140 MHz	1		1.1	FE MAC+PHY, USB 1.1 slave	1 devices		here
BCM3350	MIPS32	100 MHz	1	2002-10-18	1.1	FE MAC+PHY, USB 1.1 slave	4 devices		here
BCM3360	MIPS32	140 MHz	1	2002-11-07	1.1	FE MAC+PHY, USB 1.1 slave	8 devices		here
BCM3348	MIPS32	200 MHz	1	2003-07-09	2.0	FE MAC+PHY, USB slave	8 devices	PR	here
BCM33	MIPS32	200	1	2005-	2.0	FE MAC+PHY,	9 devices	PPage	here

Chip	Arch.	Clock speed	Cores	First seen (FCC)	DOCSIS	Notes	ESystems	PR / PPage	pdf
49		MHz		03-18		USB 1.1 slave			
BCM3361	MIPS32		1	2004-12-09	2.0	PCI IF	5 devices		
BCM3368	BMIPS4350	300 MHz	1	2006-08-29	2.0	PCI IF, USB slave	6 devices	PPage	
BCM3371	MIPS32		1	2011-03-14	2.0		1 devices		PBrief
BCM3378	BMIPS4350	320 MHz	1		2.0	DOCSIS®/EURODOCSIS™ 2.0 Voice Cable Modem SoC	0 devices	PPage	
BCM3379	BMIPS4350	320 MHz	1	2011-08-16	2.0	PCIe IF	2 devices	PPage	
BCM3380	BMIPS4350		1	2009-11-20	3.0	PCIe IF	5 devices	PPage	
BCM3381			1		2.0		0 devices	PPage	
BCM33			1	2013-	3.0	DOCSIS®/EuroDOCSIS™ 3.0	3 devices	PPage	

Chip	Arch.	Clock speed	Cores	First seen (FCC)	DOCSIS	Notes	ESystems	PR / PPage	pdf
82				02-12		Cable Gateway SoC			
BCM3383		500 MHz	1	2012-07-13	3.0	DOCSIS®/EuroDOCSIS™ 3.0 Cable Gateway SoC	8 devices	PPage	
BCM33836			1	2013-11-07	3.0	unk.	1 devices		
BCM3384			1		3.0	DOCSIS®/EuroDOCSIS™ 3.0 SoC with Full Band Capture	0 devices	PPage	
BCM33843			1	2013-12-30	3.0	DOCSIS®/EuroDOCSIS™ 3.0 Cable Gateway SoC	2 devices	PPage	

xDSL SoC

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Max. DSL capability	Notes	ESystems	PR / PPage	pdf
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Chip	Arch.	Clock speed	Cores	First seen (FCC)	Max. DSL capability	Notes	ESystems	PR / PPage	pdf
BCM6345	BMIPS3300	120 MHz	1	2003-12-15	ADSL2	FE MAC+PHY, USB 1.12+	3 devices		here
BCM6318	BMIPS3300	333 MHz	1	2013-03-06	ADSL2+		3 devices	PPage	
BCM63281	BMIPS4350[2]	320 MHz	1	2010-12-22	ADSL2+	PCIe IF, GbE switch core, 4x FE PHY	17 devices		
BCM63283	MIPS32	320 MHz	1	2011-03-11	ADSL2+	PCIe IF, GbE switch core, 4x FE PHY	1 devices		
BCM6332	BMIPS3300	240 MHz	1	2010-09-07	ADSL2+		7 devices		
BCM6333	BMIPS3300	240 MHz	1	2009-07-24	ADSL2+	SDIO IF	4 devices		
BCM63	BMIPS3300	240	1	2008-	ADSL2+	SDIO IF	4 devices	PPage	here

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Max. DSL capability	Notes	ESystems	PR / PPage	pdf
38	0	MHz		02-25					
BCM6348	BMIPS330 0	240 (S)/ 256 MHz	1	2004-11-06	ADSL2+	2x FE MAC (1 PHY), USB 1.1, PCI, PCMCIA	45 devices	PPage	1 , 2
BCM6358	BMIPS435 0	300 MHz	2 (shared TLB)	2005-11-02	ADSL2+	2x FE MAC+PHY, 2x USB 2.0 IF, PCI	33 devices	PPage	
BCM6359	BMIPS435 0	300 MHz	2 (shared TLB)	2007-12-21	??	Lacks xDSL AFE	1 devices		
BCM6368	BMIPS435 0	400 MHz	2	2009-05-07	VDSL2 / ADSL2+	Multi-core, USB 2.0 host + slave, GbE switch core, 4x FE PHY, PCI	12 devices	PPage	

Chip	Arch.	Clock speed	Cores	First seen (FCC)	Max. DSL capability	Notes	ESystems	PR / PPage	pdf
BCM6369	BMIPS4350	400 MHz	2		VDSL2 / ADSL2+ (?)	Lacks xDSL AFE	1 devices		

PLC

Chip	Company on chip	PHY rate (Powerline)	PHY rate (Coax)	Ethernet Link Speed	First seen (FCC)	Notes	ESystems	PR / PPage
BCM60301	-	200 Mbps	-	-			0 devices	PPage
BCM60321	-	200 Mbps	-	-	2012-01-19		7 devices	PPage

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