VE8910

Single Channel FXS Chipset

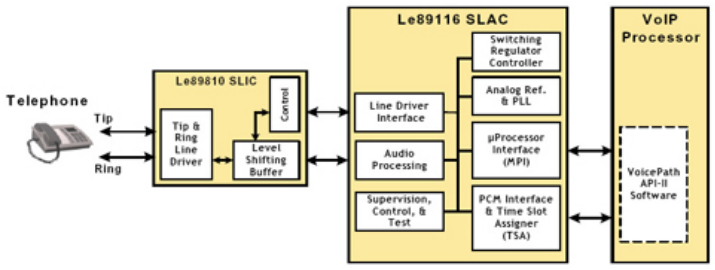
Not recommended for new designs.
Use miSLIC Series instead.

The VE8910 chipset is a cost-optimized 1FXS chipset for residential VoIP gateways such as voice enabled DSL modems, set-top boxes, and analog terminal adapters.

The chipset implements FXS functionality by providing the necessary voice interface functions to connect to and power a telephone. On the digital side, the VE8910 chipset provides standard MPI and PCM interfaces to a VoIP processor.

The VE8910 chipset, coupled with the VoicePath API-II (VP API-II) enables designers to offer a single hardware design that is software programmable for worldwide markets. The VP API-II 'C' code is used to abstract the devices from application code while providing functions for controlling, supervising, and testing a set of subscriber lines.

Detailed Block Diagram



Features & Benefits

- Cost-optimized 1FXS chipset for VoIP access devices
- Implements all the key BORSHT functions
- Built-in DC/DC controller configurable for buck-boost or flyback operation

- Integrated balanced ringing generator capable of driving 5 REN at 70 VPK or 3 REN at 92 VPK
- Standard 8-kHz and Wideband 16-kHz sample rates
- Low power consumption in all modes. Typical on-hook standby power consumption is 120 mW, less than half that of competing solutions
- Single hardware design with software support for worldwide market
- VoicePath API-II Software
  - Significantly reduces development and testing time
  - Enables modular designs based on the VE8910 and other members of the VE890 Series for1FXS, 1FXS+1FXO, and 2FXS+1FXO product variants
  - Allows for a seamless migration between products using a common software architecture
- Supported by SDK, development board, and reference designs
- Support for GR-909/TIA-1063 metallic loop (line) testing using VeriVoice Test Suite software

Products

- Voice Line Circuits
  - miSLIC Series – High Performance Line Circuits
  - ZL880 – Enhanced Dual Channel Wideband FXS Line Interfaces
  - VE880 – FXS and FXO Line Interfaces
  - VE890 – Integrated FXS/FXO Line Interface
    - LE89156
    - VE8901
    - VE8910
    - VE8910-HV
    - VE8911
    - VE8911-HV
    - VE8921
    - VeriVoice Test Software
  - VE950 – General Purpose Ringing SLIC
  - VE792 – Next Generation Carrier Chipset
  - VE790 – High Performance Programmable Chipset
  - VE750 – Line Card Access Switches
  - VE580 – General Purpose SLICs and Codec
  - VE770 – SLIC/Codecs with (DTMF)
  - Integrated Fixed Companding Codecs
  - Multi-Featured Programmable Phone Codecs
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